

DESIGNING MULTI-RESOURCE ARBITER

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SUMMARY

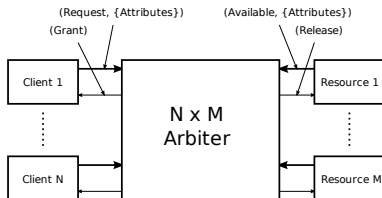
- 1 INTRODUCTION
- 2 ARBITER DESIGN
- 3 VERIFICATION AND ANALYSIS
- 4 CONCLUSIONS

IDEA OF ARBITRATION

Some resources is used by many agents (clients). However, not more than one such agent can actually use the resource at the same time. Arbiter is the entity that manages client access.

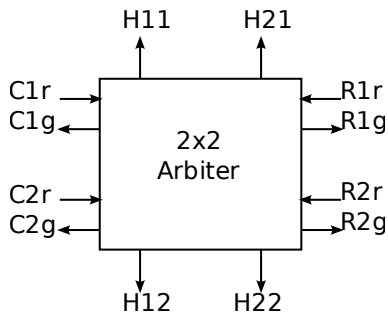
- Cars + Cross roads → Traffic lights
- Processes + Critical sections in the program code → Semaphores
- Data + Memory, Processor, Data channels → Arbiters

GENERAL ARBITER STRUCTURE



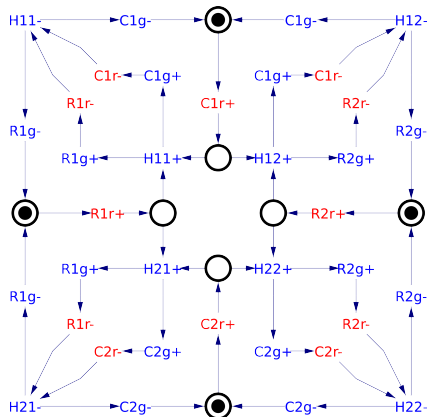
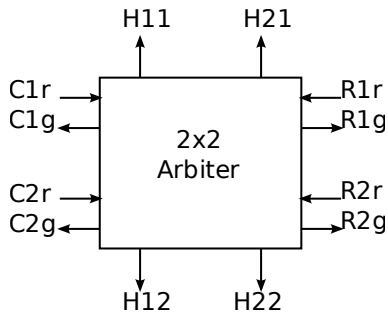
- Resources inform about their availability
- Arbiter responds by allowing certain pairs of handshakes

TASK SPECIFICATION

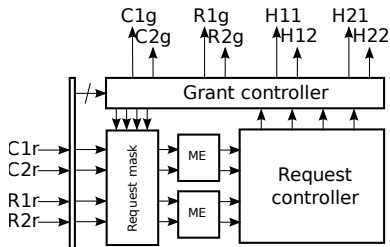


- Design the 2×2 multi-resource arbiter
- Find SI, asynchronous implementation
- Allow simultaneous handshakes
- Do not allow conflicting handshakes
- Minimise response latency

TASK SPECIFICATION

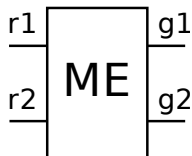


OVERALL STRUCTURE



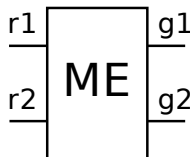
- MUTEX elements ensure not more than one channel can be selected by the request controller
- Request controller selects the channel to be activated
- Grant controller returns grant signals and activates the request mask
- Request mask hides initial requests to free MUTEX-es and open the Request controller for further requests

KEY COMPONENTS

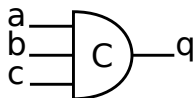


- MUTEX element
- Three-input C-element
 $q = a \cdot b \cdot c + q \cdot (a + b + c)$
- Asymmetric C-element
 $q = a \cdot b \cdot c \cdot d + q \cdot (c + d)$

KEY COMPONENTS



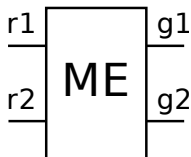
- MUTEX element



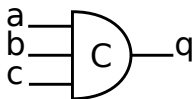
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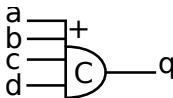
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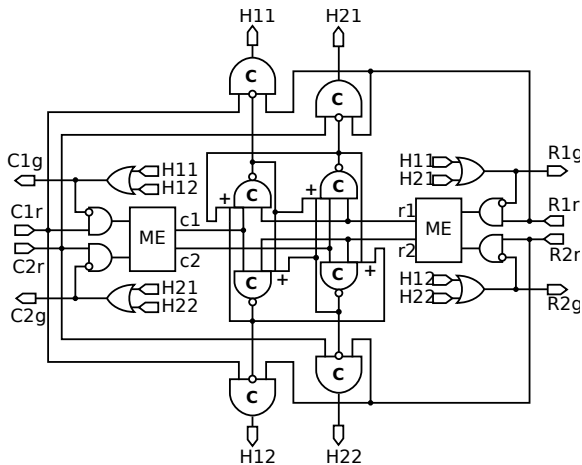


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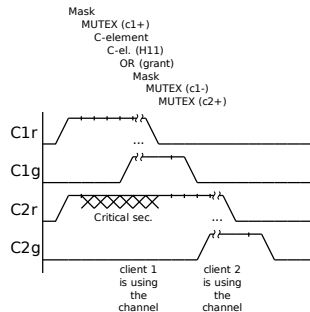
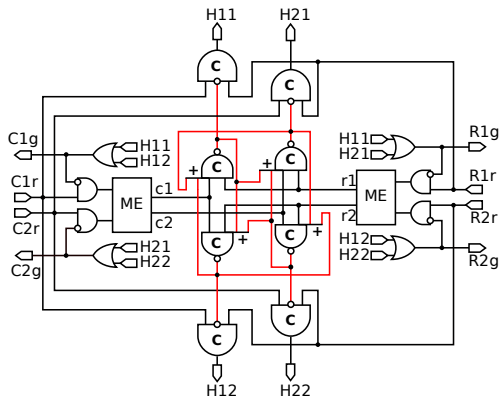


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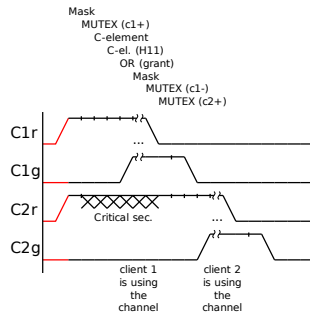
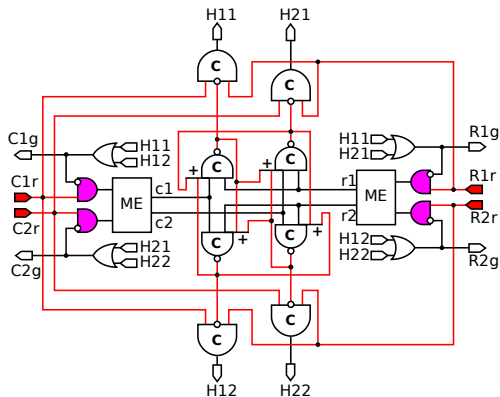
IMPLEMENTATION



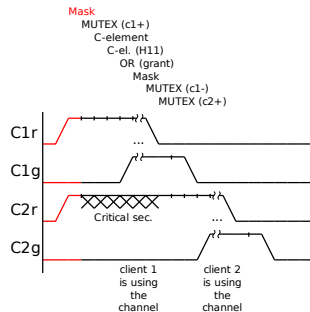
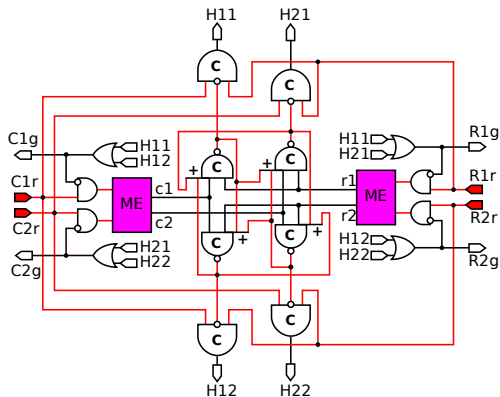
TIMING ESTIMATION



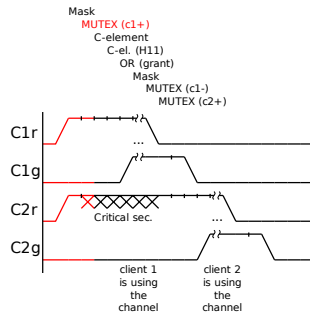
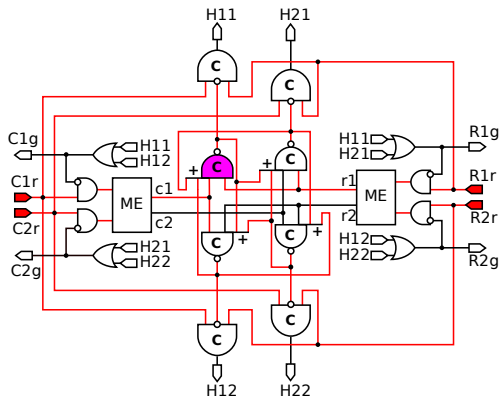
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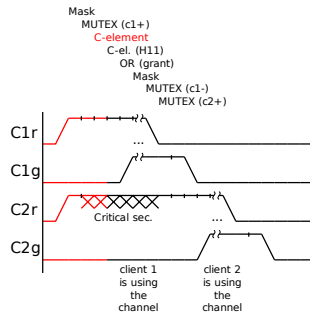
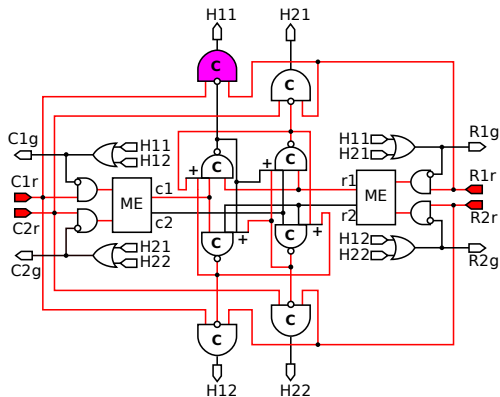
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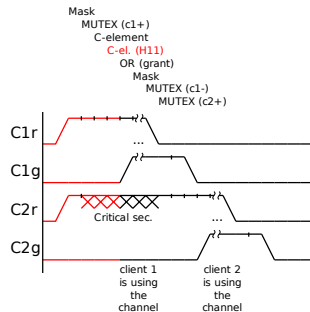
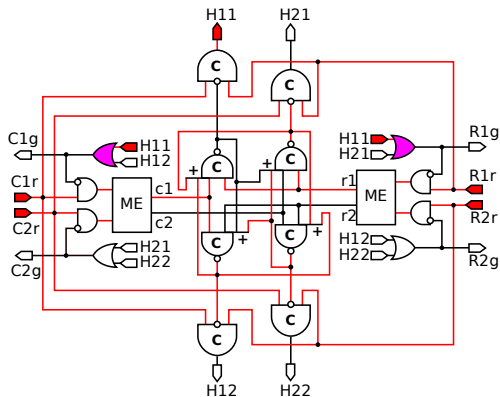
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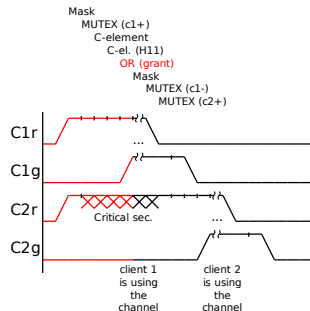
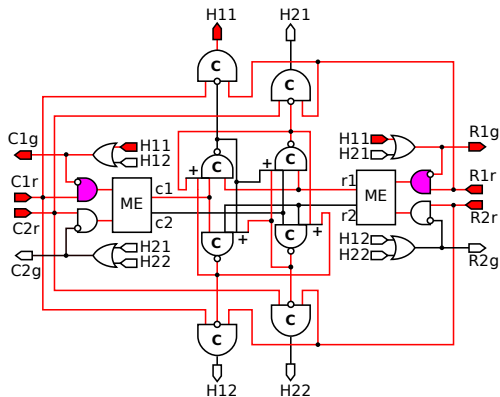
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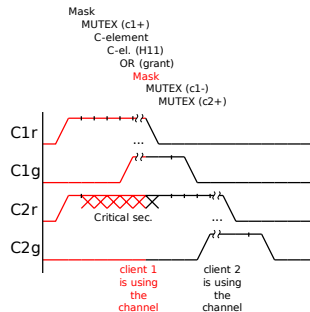
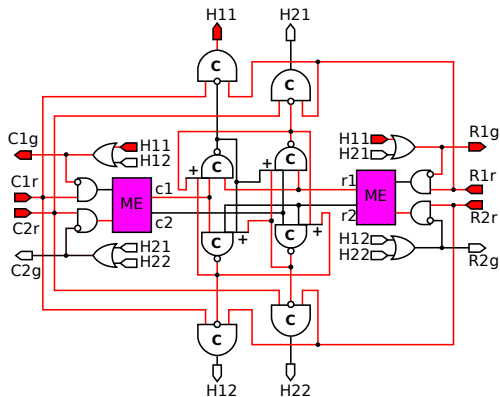
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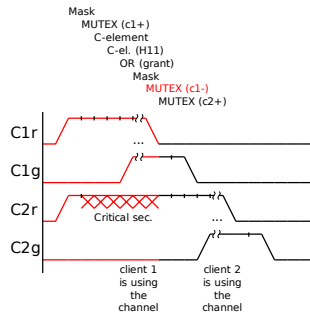
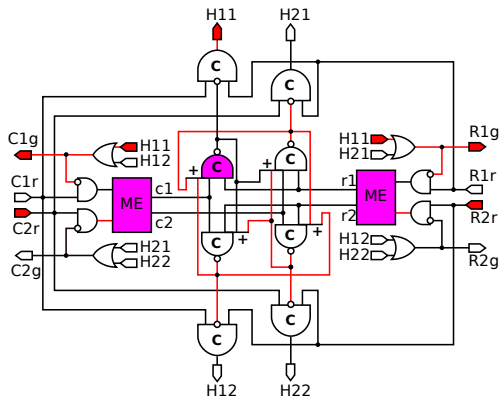
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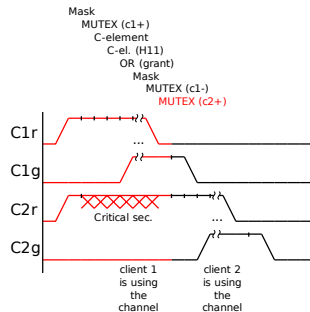
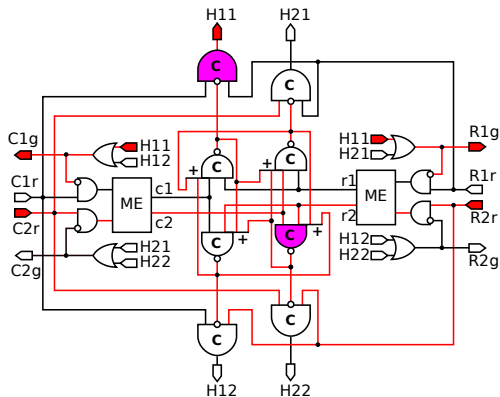
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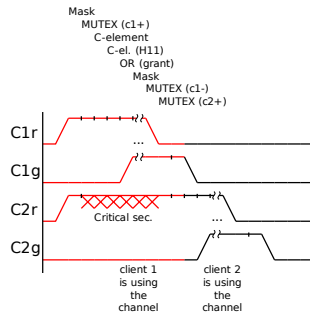
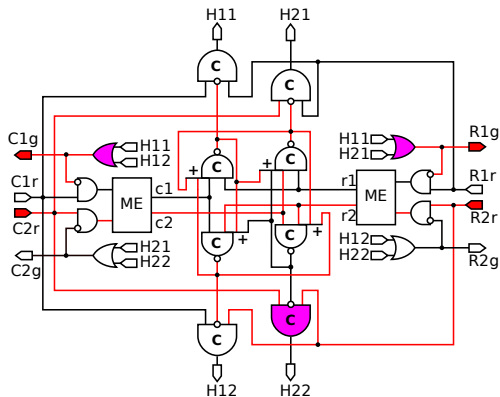
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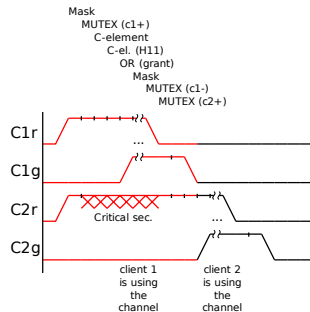
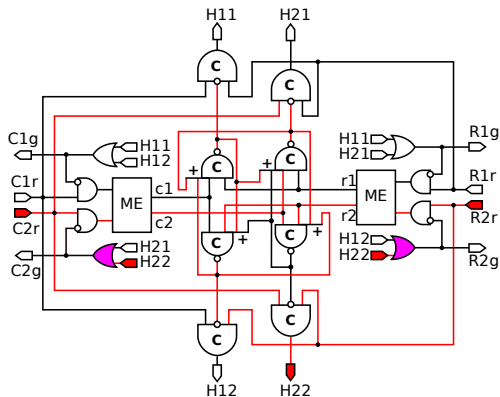
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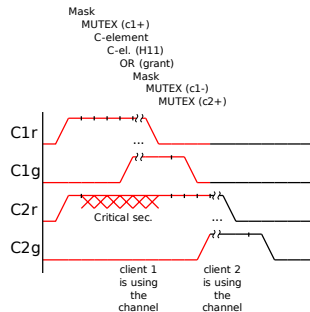
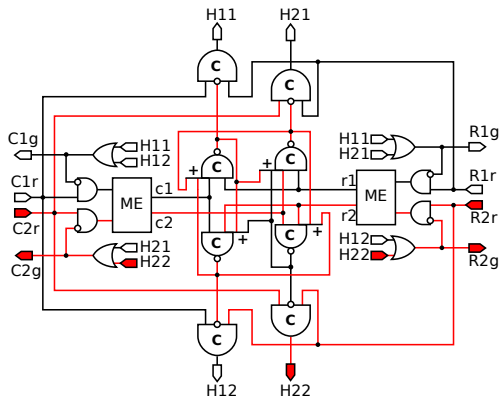
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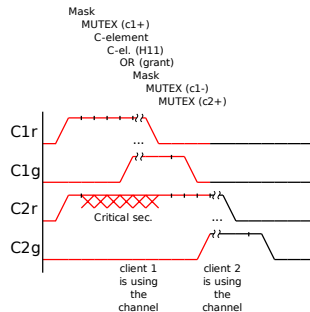


TIMING ESTIMATION



TIMING ESTIMATION

- 5 to 11 gates delays, with the critical section of 6 gate delays

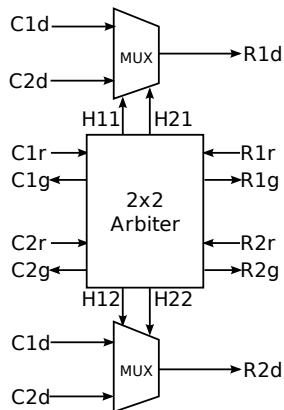


PROVING TO BE SPEED-INDEPENDENT

- The arbiter was verified using the approach presented in [ASYNC'08]:
 - Gate Level Model
 - Generate circuit Petri Net
 - Composition with the environment STG
 - Unfolding of the obtained Petri net
 - Reachability analysis on the obtained unfolding

EXAMPLE OF USAGE 1

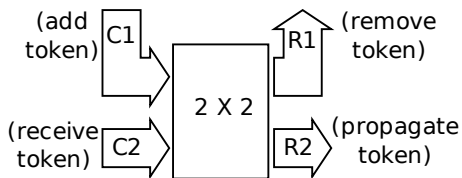
DATA TRANSPORTATION



- Handshakes form dual rail signal
- Multiplexers select corresponding channel for data transport from clients to resources
- Useful for data producer/consumer interactions, for instance, processors executing instructions

EXAMPLE OF USAGE 2

MULTI-TOKEN ARBITER



- Forms busy token ring
- Clients add and remove tokens
- Needs design modification to disable $C1 \rightarrow R1$



EXTENDING THE DESIGN

- To extend the design to $N \times M$ inputs it is necessary to use N and M input arbiters
- More complicated structure of channel controller and grant controller
- The arbiter will have $N \times M$ different channel outputs
- **Problem:** Each additional client or resource will linear increase the response time, hence, is only applicable for small increase of inputs
- Solve the problem using approach similar to Priority Arbiters?

CONCLUSIONS

- The SI implementation for 2×2 arbiter case is found
- Arbiter can be useful to balance load between data producers and consumers. Alternatively, can form multi-token arbiter ring structure
- The effective $N \times M$ arbiter implementation still needs to be found

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THANK YOU!

Questions?