Teak components and networks

There are only a small number of templates for Teak components. The component set was chosen to keep each control feature to a single component.

For example, to add two numbers together, an Operator (O) must have its inputs Join-ed (J) together as it doesn’t perform that control function itself, only the Join (J) component does.

The Teak tool can currently generate gate-level Verilog implementations of Teak networks.

The Teak component set was chosen to allow Teak networks to be easier to implement than Handshake Circuits in other, more pipeline-oriented, asynchronous technologies.