CP15 register transfer instructions

31–28	27–24	23–21	20	19–16	15–12	11–8	7–5	4	3–0
cond	1 1 1 0	0 0 0	L	CRn	Rd	1 1 1 1	Cop2	1	CRm

L: load from coprocessor/store to coprocessor

CP15 register 0

31–24	23–16	15–4	3–0
implementer	0 0 0 0 0 0 0 A	part number (BCD)	revision

CP15 register structure

Register	Purpose
0	ID Register
1	Control
2	Translation Table Base
3	Domain Access Control
4	UNUSED
5	Fault Status
6	Fault Address
7	Cache Operations
8	TLB Operations
9 to 15	UNUSED

CP15 register 1

31 13	12	11	10	9	8	7	6	5	4	3	2	1	0
0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	I	Z	F	R	S	B	L	D	P	W	C	A	M

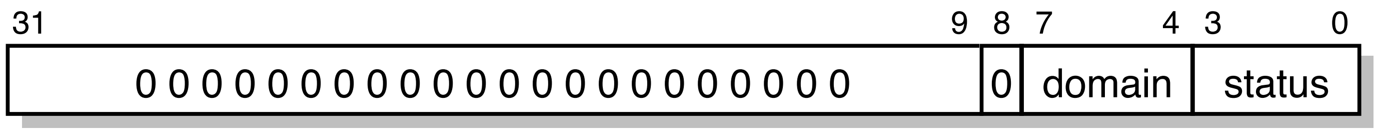
CP15 register 2

31 – 14	13 – 0
translation table base address	0 0 0 0 0 0 0 0 0 0 0 0 0 0

CP15 register 3

31 30	29 28	27 26	25 24	23 22	21 20	19 18	17 16	15 14	13 12	11 10	9 8	7 6	5 4	3 2	1 0
D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0

CP15 register 5



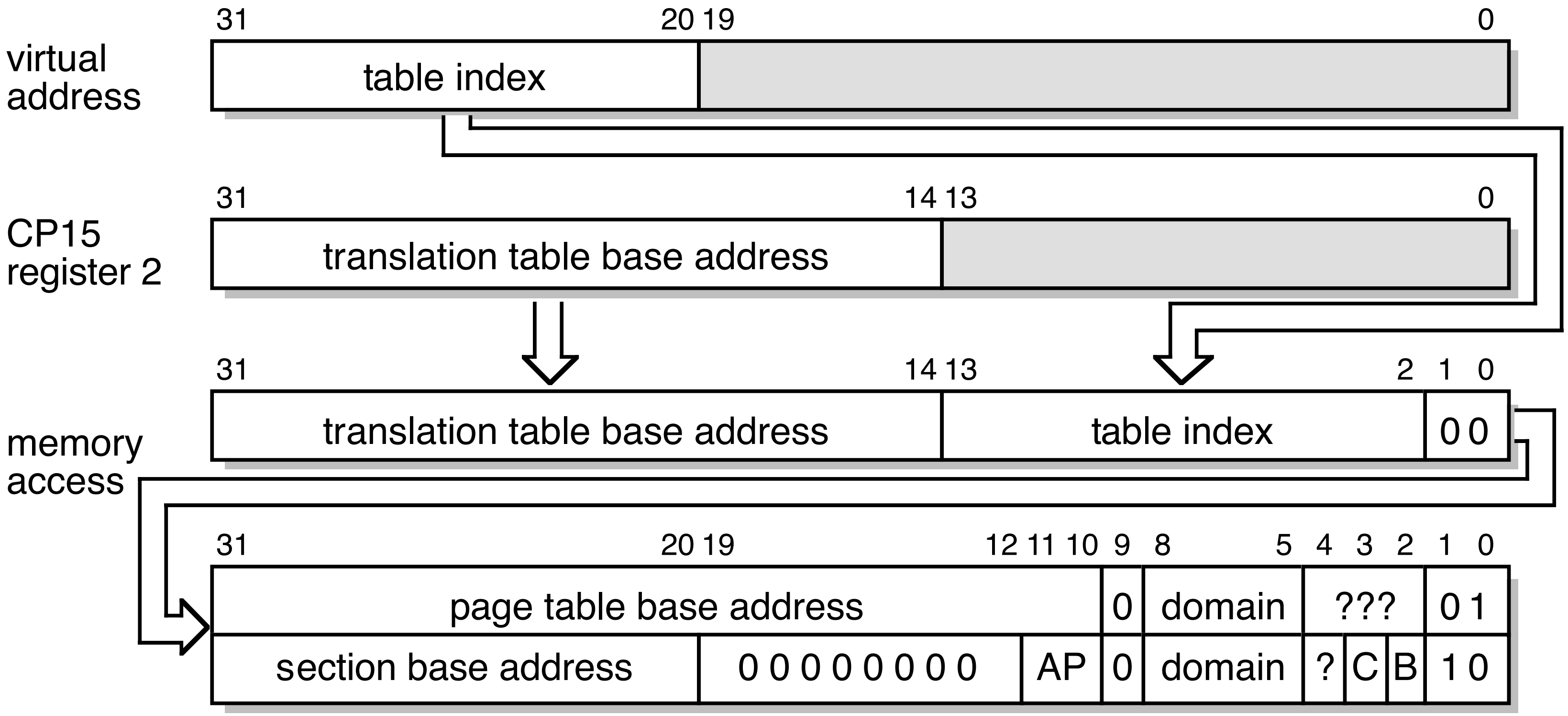
CP15 register 6

31 – 0
fault address

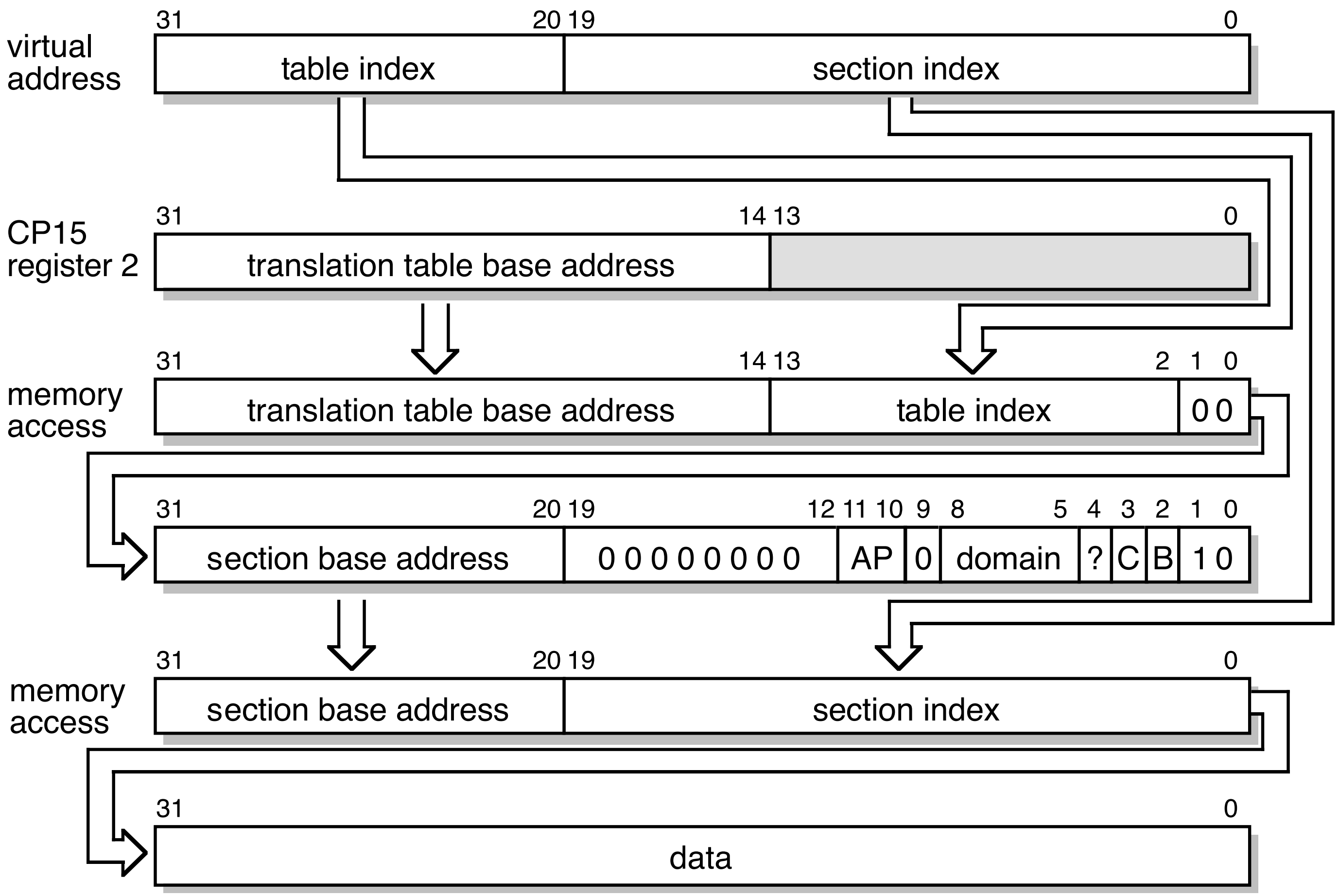
Domain access control bits

Value	Status	Description
00	No access	Any access will generate a domain fault
01	Client	Page and section permission bits are checked
10	Reserved	Do not use
11	Manager	Page and section permission bits are not checked

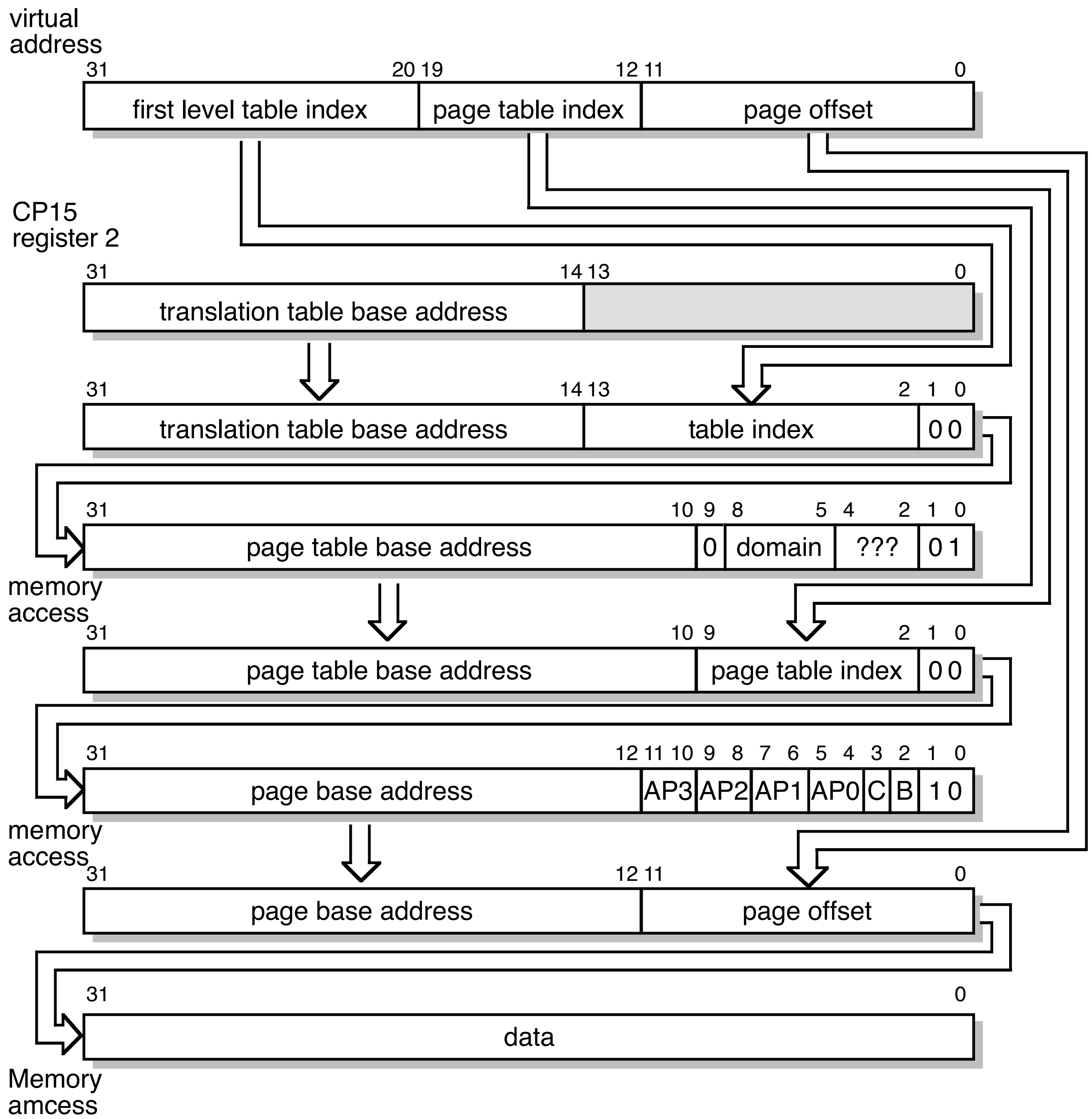
First-level translation fetch



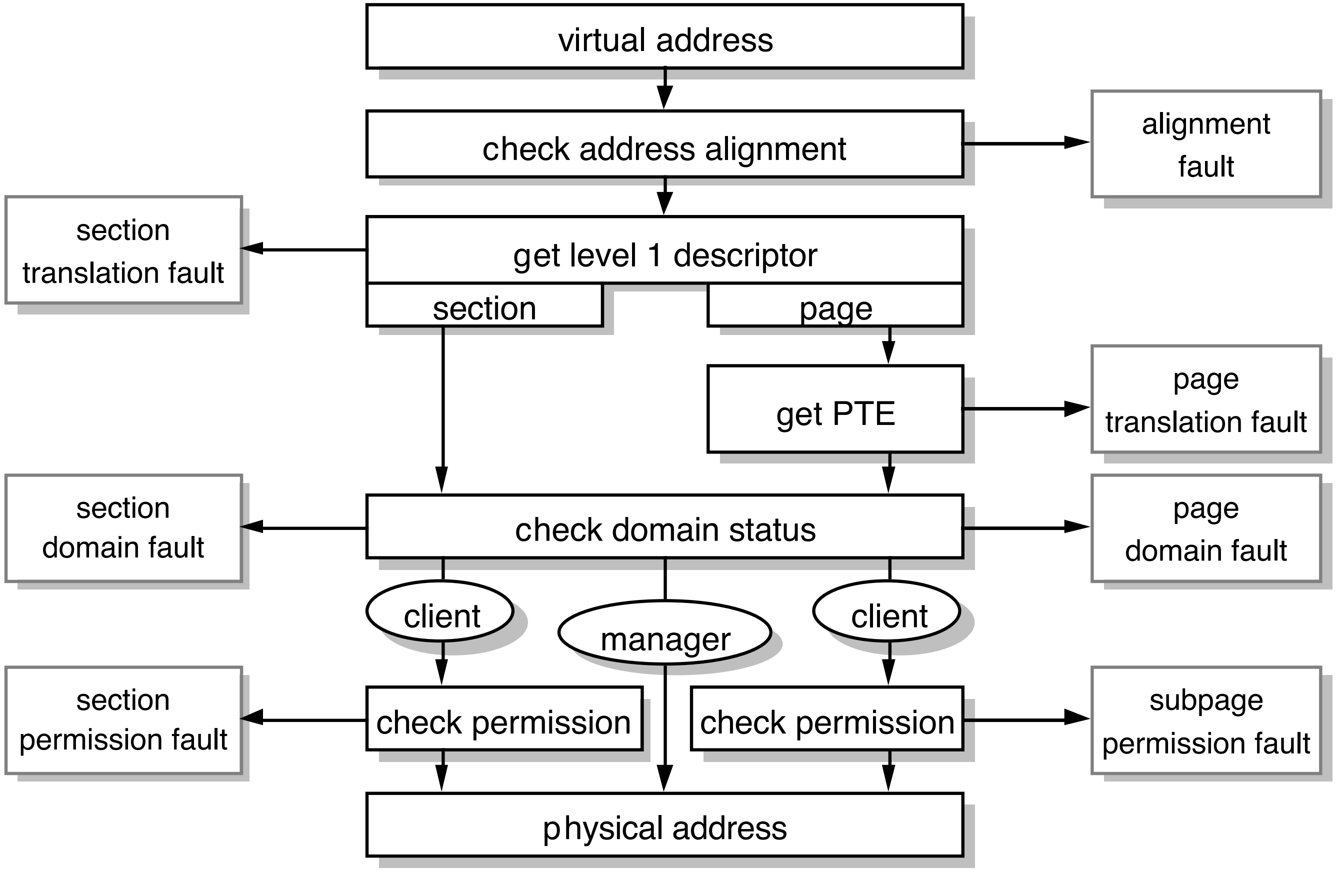
Section translation sequence



Small page translation sequence



Access permission checking scheme



Access permissions

AP	S	R	Supervisor	User
00	0	0	No access	No access
00	1	0	Read only	No access
00	0	1	Read only	Read only
00	1	1	Do not use	
01	-	-	Read/write	No access
10	-	-	Read/write	Read only
11	-	-	Read/write	Read/write